

```

graph TD
    subgraph Computing_Device [COMPUTING DEVICE 1]
        Stereopairs[STEREOPAIRS 8] --> Memory[MEMORY UNIT 12]
        Memory <--> Processing[PROCESSING BLOCK 14]
        Processing <--> Control[CONTROL OF LC PANELS AND LIGHTING UNIT 16]
        ViewerSignal[VIEWER POSITION SIGNAL 10 (OPT.)] --> Processing
    end
    Control --> Panel2[2]
    Control --> LCD4[LCD 4]
    Control --> LCD6[LCD 6]

```

FIGURE 2

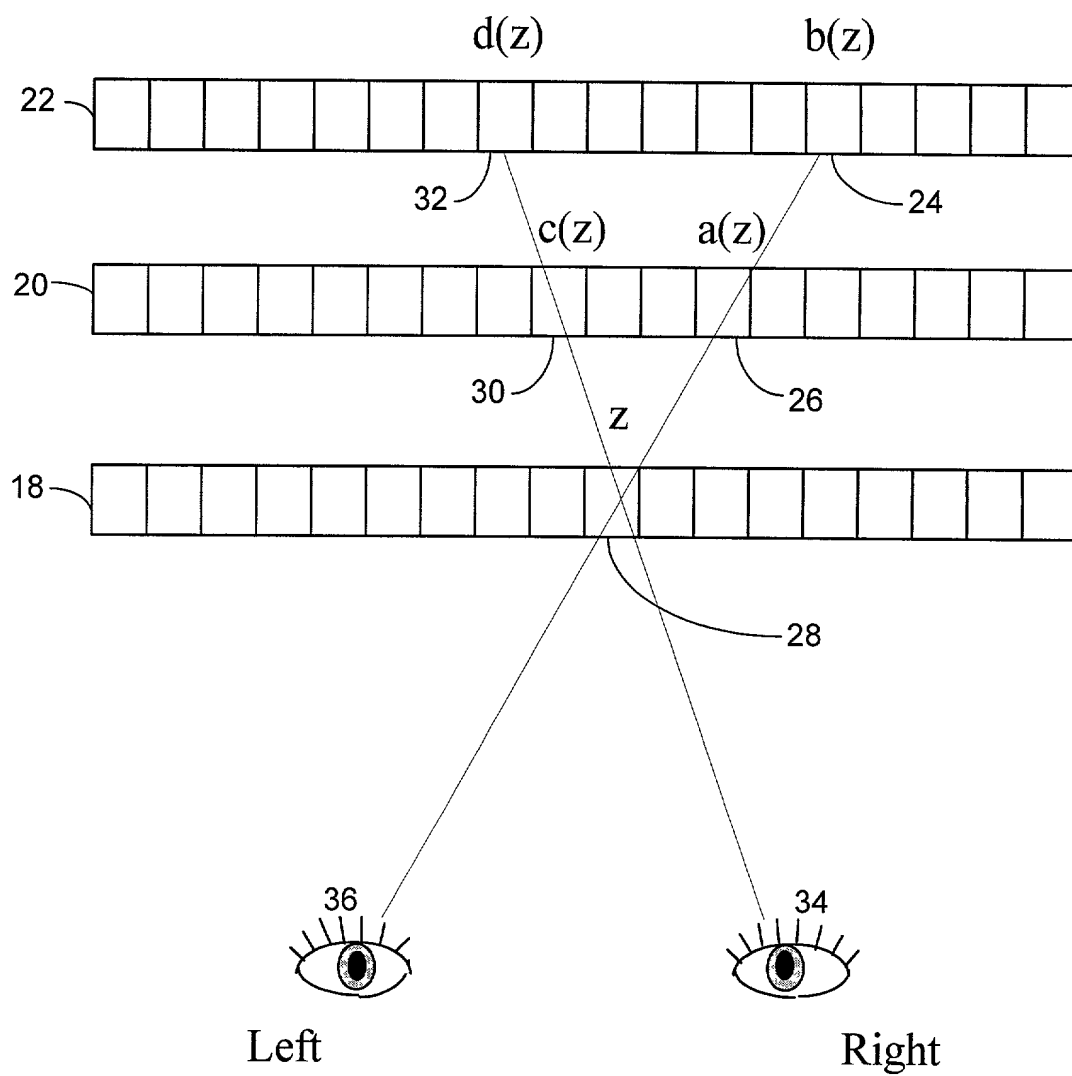


FIGURE 3

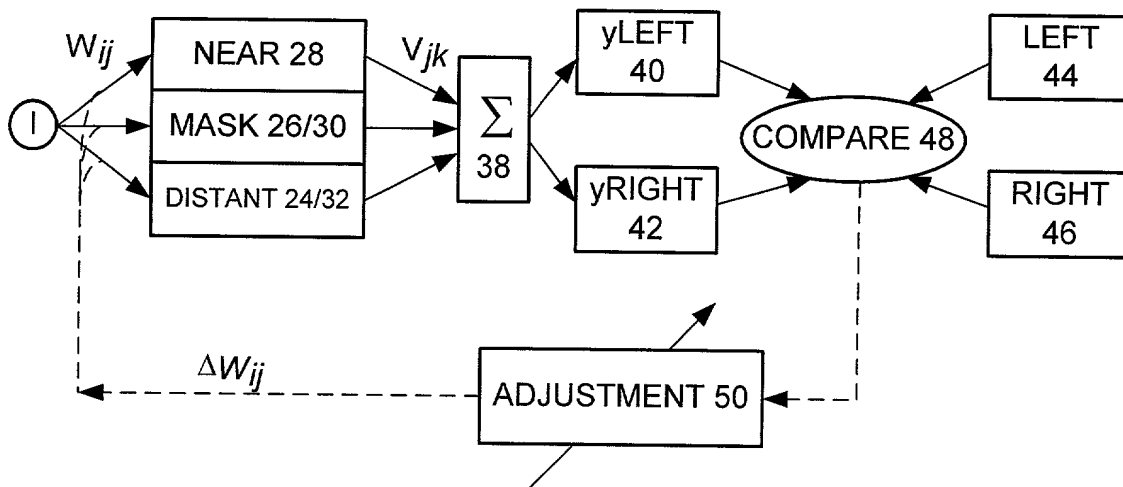


FIGURE 4

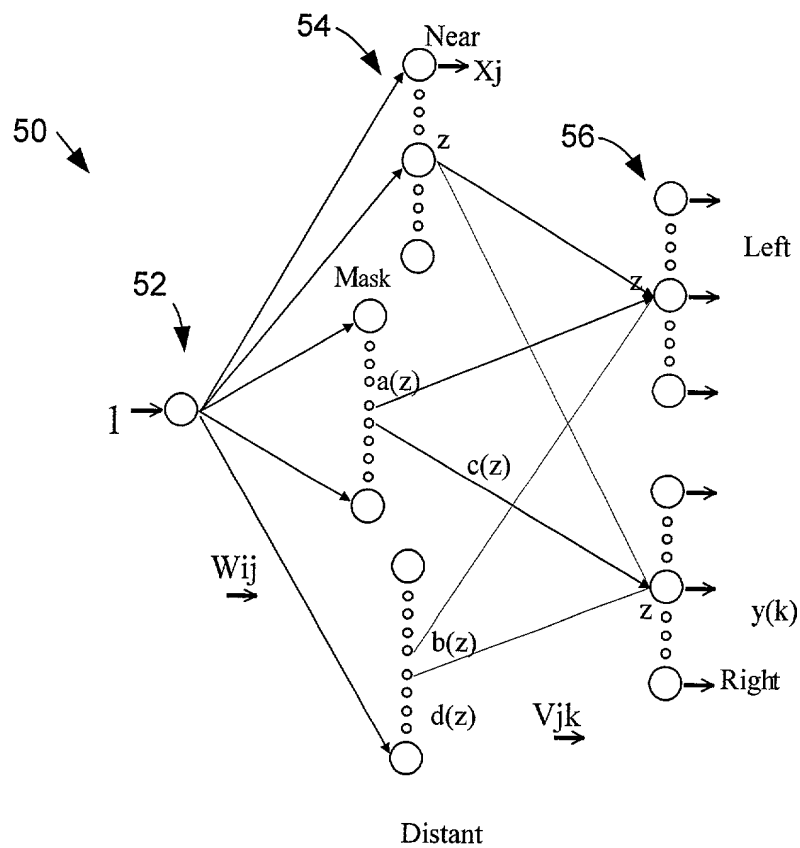


FIGURE 5